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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

Confirmation No.: 5935

Suguru TACHIBANA et al.

Art Unit: 2838

Serial No.: 10/808,532

Examiner: B. Vu

Filed: March 25, 2004

Docket No.: 100353-00185

For: SEMICONDUCTOR INTEGRATED CIRCUIT USING BAND-GAP  
REFERENCE CIRCUIT (*As Amended*)

**PETITION FOR EXTENSION OF TIME**

Mail Stop AMENDMENT  
Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 19, 2005

Sir:

Applicants petition the Commissioner of Patents and Trademarks to extend the time for responding to the Office Action dated June 27, 2005, for one (1) month from September 27, 2005 to October 27, 2005.

A check in the amount of One Hundred and Twenty Dollars (\$120.00), covering the extension fee, is enclosed. In the event any fees are required, please charge our Deposit Account No. 01-2300.

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120.00 OP

Respectfully submitted,

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TJC/klf